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1. The terminal disclaimer, submitted on 12/27/00 in paper No. 7, has been reviewed and accepted.
2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless --

3. (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
4. Claim 1 is rejected under 35 U.S.C. 102(b) as being anticipated by Dola et al.
5. The disclosure of Dola provides a complete response to each and every element set forth in this claim. For example: Figs. 1 & 3 show a receptacle housing 11, a first face 12, a second face 13, a plurality of conductive elements 33, each extending from the first face to the second face, a header 38, a conductive housing 45, end walls 39, 42, a medial wall 46, passages 48, signal pins 52 and a ground element (41, 49).
6. Applicant's arguments filed 12/20/00 have been fully considered but they are not persuasive.

In response to the remarks on pages 3-4, applicant argues that conductive elements 33 of Dola do not extend from the first face to the second face. This argument is not persuasive because Fig. 3 clearly shows conductive elements 33 extend from the first face 12 to the second face 13. The other remarks are considered to be fully addressed in the rejected claim above.

7. Claim 27 is allowed.

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8. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire **THREE MONTHS** from the mailing date of this action. In the event a first reply is filed within **TWO MONTHS** of the mailing date of this final action and the advisory action is not mailed until after the end of the **THREE-MONTH** shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than **SIX MONTHS** from the date of this final action.

9. Any inquiry concerning this communication should be directed to Hien Vu at telephone number (703) 308-2009.

Vu/nt

3-15-01



Hien Vu

Art Unit: 2833

1. The nonstatutory double patenting rejection is based on a judicially created doctrine grounded in public policy (a policy reflected in the statute) so as to prevent the unjustified or improper timewise extension of the "right to exclude" granted by a patent and to prevent possible harassment by multiple assignees. See *In re Goodman*, 11 F.3d 1046, 29 USPQ2d 2010 (Fed. Cir. 1993); *In re Longi*, 759 F.2d 887, 225 USPQ 645 (Fed. Cir. 1985); *In re Van Ornum*, 686 F.2d 937, 214 USPQ 761 (CCPA 1982); *In re Vogel*, 422 F.2d 438, 164 USPQ 619 (CCPA 1970); and, *In re Thorington*, 418 F.2d 528, 163 USPQ 644 (CCPA 1969).

A timely filed terminal disclaimer in compliance with 37 CFR 1.321(c) may be used to overcome an actual or provisional rejection based on a non-statutory double patenting ground provided the conflicting application or patent is shown to be commonly owned with this application. See 37 CFR 1.130(b).

Effective January 1, 1994, a registered attorney or agent of record may sign a terminal disclaimer. A terminal disclaimer signed by the assignee must fully comply with 37 CFR 3.73(b).

2. Claim 26 is rejected under the judicially created doctrine of obviousness-type double patenting as being unpatentable over claim 1 of U.S. Patent No. 5,967,844. Although the conflicting claims are not identical, they are not patentably distinct from each other because to form the insulative plate to be a vertical plane would have been an obvious of modification since such change produces same result.

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless --

4. (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

5. Claim 1 is rejected under 35 U.S.C. 102(b) as being anticipated by Dola et al.

6. The disclosure of Dola provides a complete response to each and every element set forth in this claim. For example: Figs. 1 & 3 show a receptacle 11, a plurality of conductive means 33,

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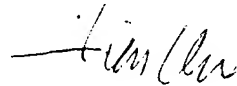
a header 38, a conductive housing 45, end walls 39, 42, a medial wall 46, passway 48, signal pins 52 and a grounding means (41, 49).

7. Czeschka, Champion et al, Mair and Zell et al are cited for disclosure of shielded connectors.

8. Any inquiry concerning this communication should be directed to Hien Vu at telephone number (703) 308-2009.

Vu/ds

09/13/00



Hien Vu  
Primary Examiner



US006322393B1

(12) **United States Patent**  
**Doutrich et al.**

(10) Patent No.: **US 6,322,393 B1**  
(45) Date of Patent: **\*Nov. 27, 2001**

(54) **ELECTRICALLY ENHANCED MODULAR  
CONNECTOR FOR PRINTED WIRING  
BOARD**

(75) Inventors: **Ray C. Doutrich**, Lebanon; **Stuart C. Stoner**, Lewisberry; **Peter K. Townsend**, Camp Hill; **David F. Fusselman**, Middletown, all of PA (US)

(73) Assignee: **FCI Americas Technology, Inc.**, Reno, NV (US)

(\*) Notice: Subject to any disclaimer, the term of this patent is extended or adjusted under 35 U.S.C. 154(b) by 0 days.

This patent is subject to a terminal disclaimer.

(21) Appl. No.: **09/358,864**

(22) Filed: **Jul. 22, 1999**

**Related U.S. Application Data**

(62) Division of application No. 08/417,085, filed on Apr. 4, 1995, now Pat. No. 5,967,844.

(51) Int. Cl.<sup>7</sup> ..... **H01K 13/648**

(52) U.S. Cl. .... **439/607; 439/108**

(58) Field of Search ..... **439/607-610, 439/101, 108, 95**

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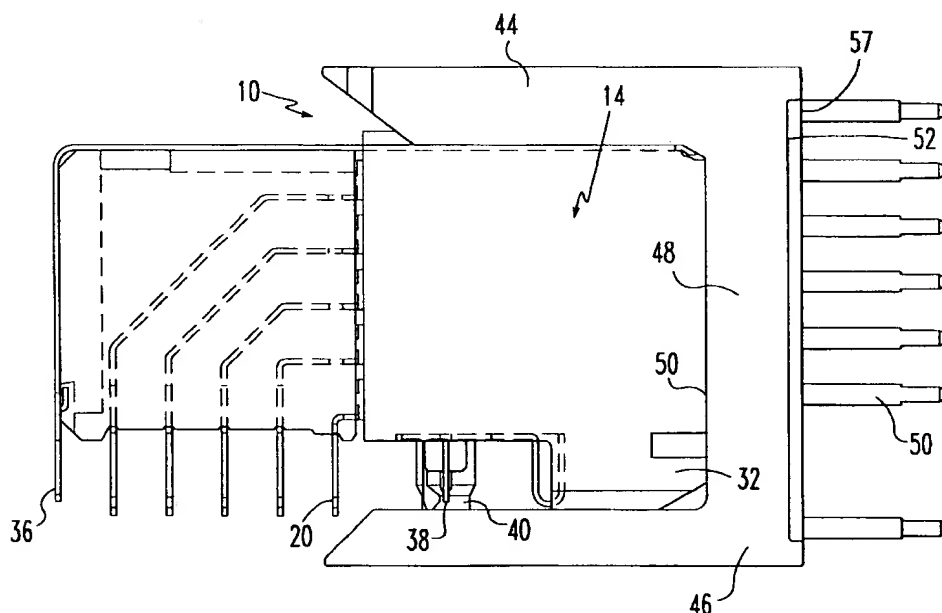
Primary Examiner—Hien Vu

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Mackiewicz & Norris LLP

(57) **ABSTRACT**

An electrical connector for connecting a daughter and a mother printed wiring board. A receptacle is connected to one board on one face and to the header on the other. The header has a conductive housing with opposed end walls and a medial pin receiving wall where it is connected to the other board. The receptacle housing is electrically connected to the header conductive housing by removable springs extending from the end walls of the header housing and separate removable grounding pins extending to a printed wiring board from the medial wall.

**1 Claim, 8 Drawing Sheets**



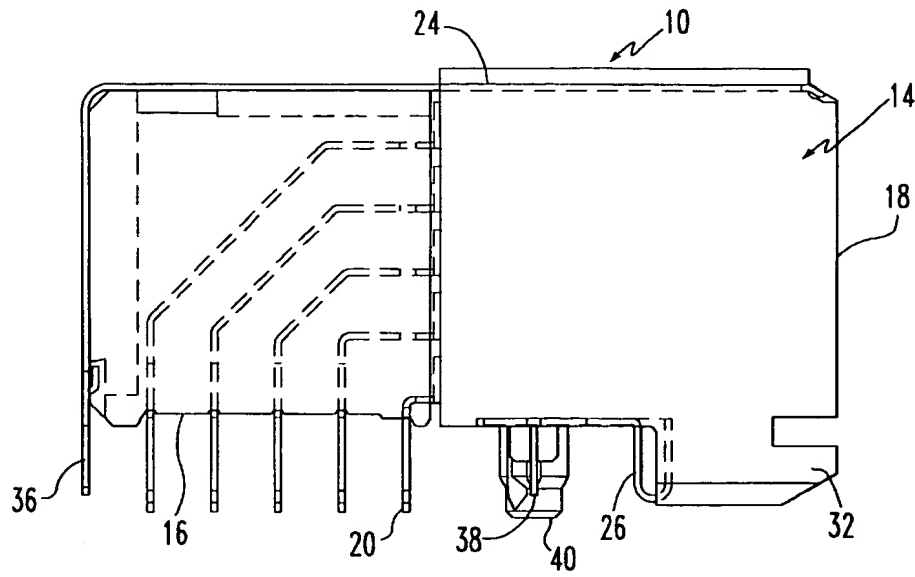


FIG. 1

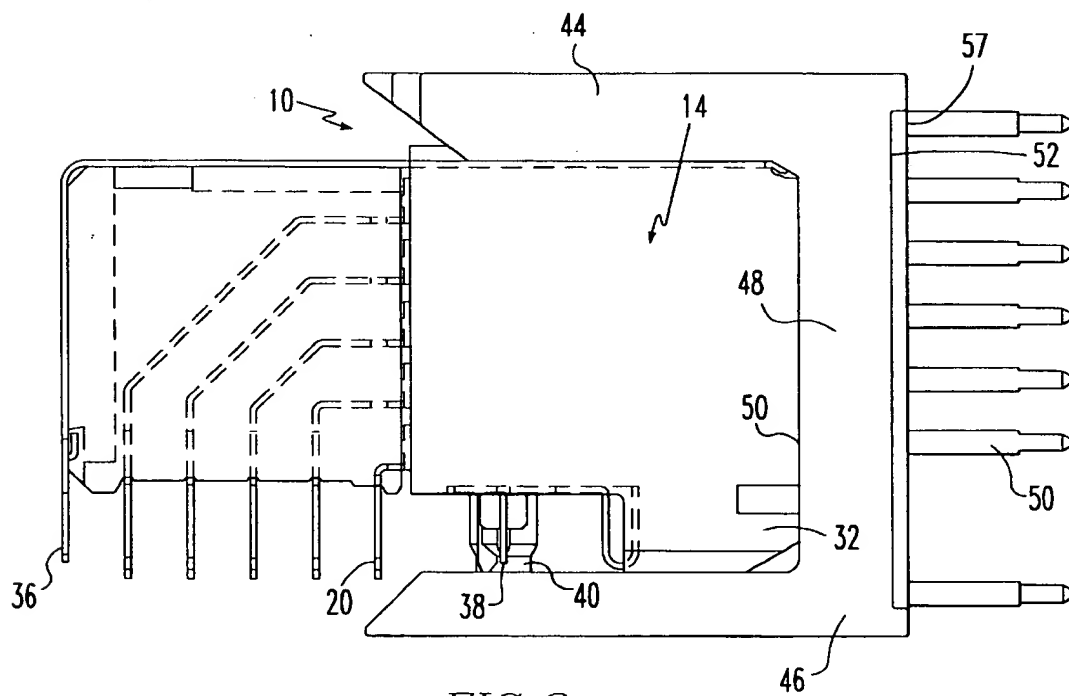


FIG. 3

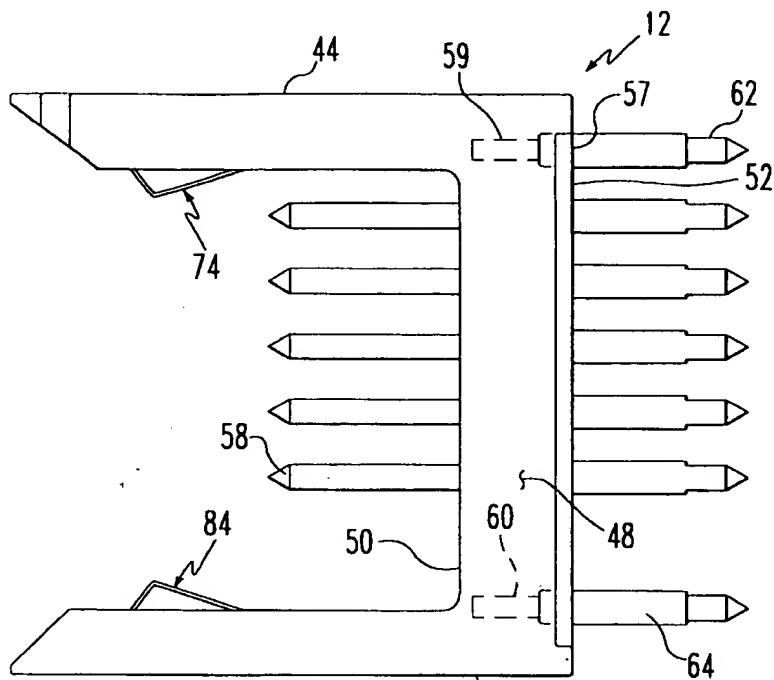


FIG. 2

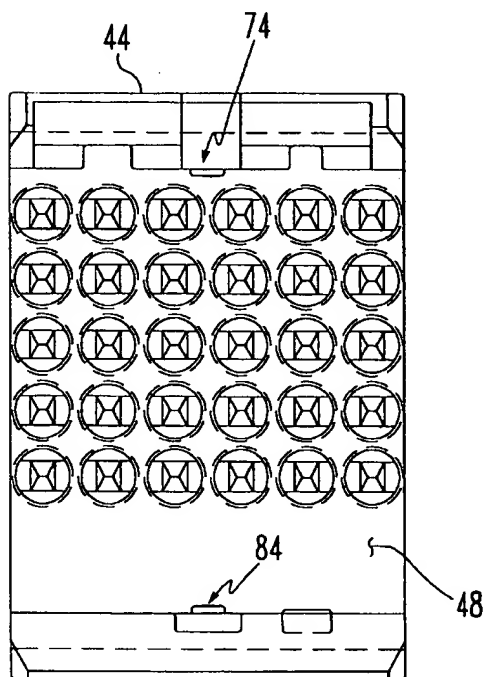


FIG. 9



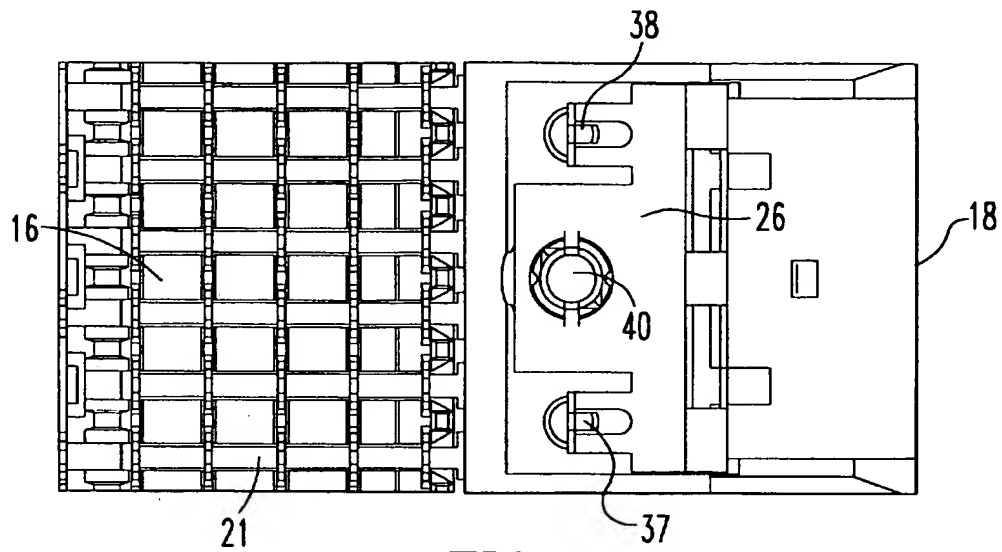


FIG. 4

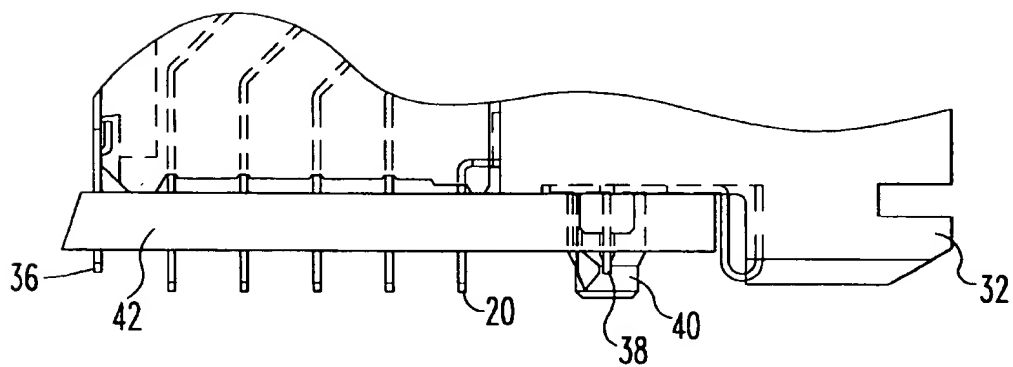


FIG. 7

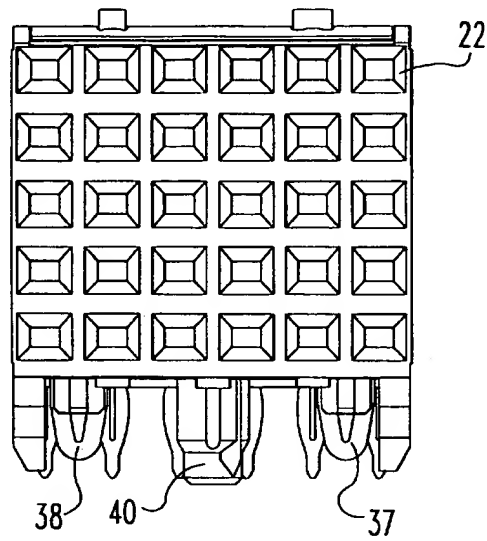


FIG. 5

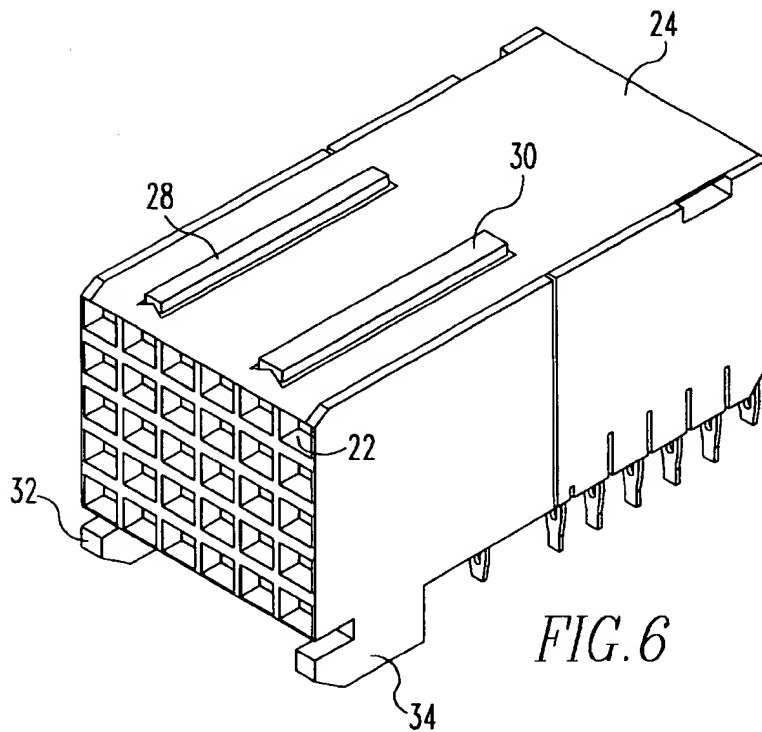


FIG. 6

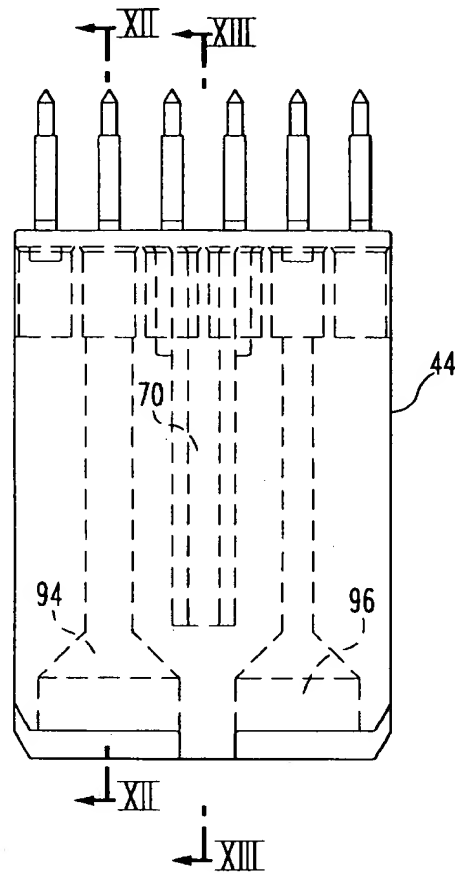


FIG. 8

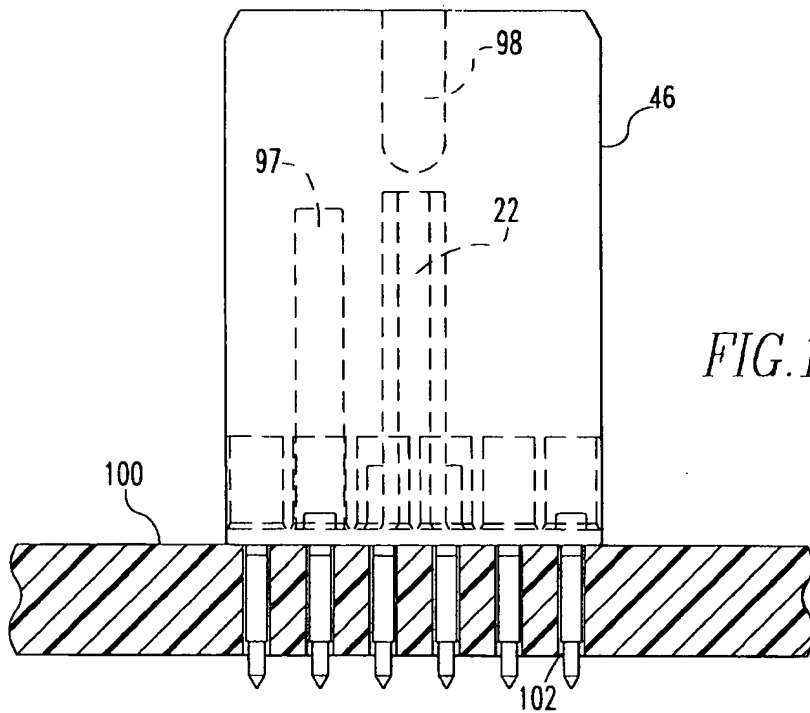
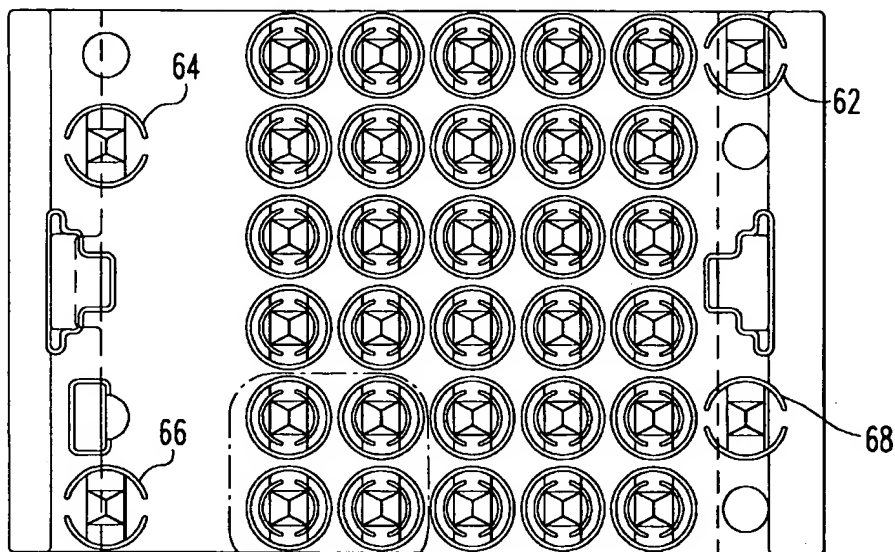


FIG. 10



XIV

FIG. 11

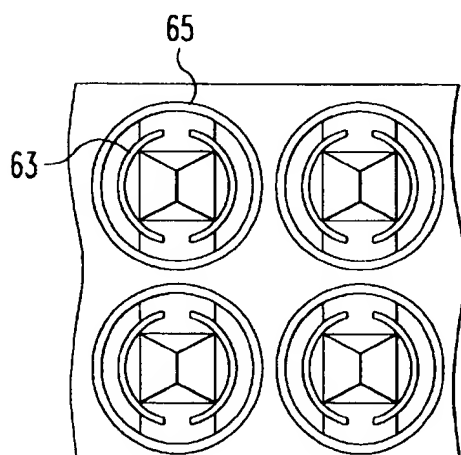


FIG. 14

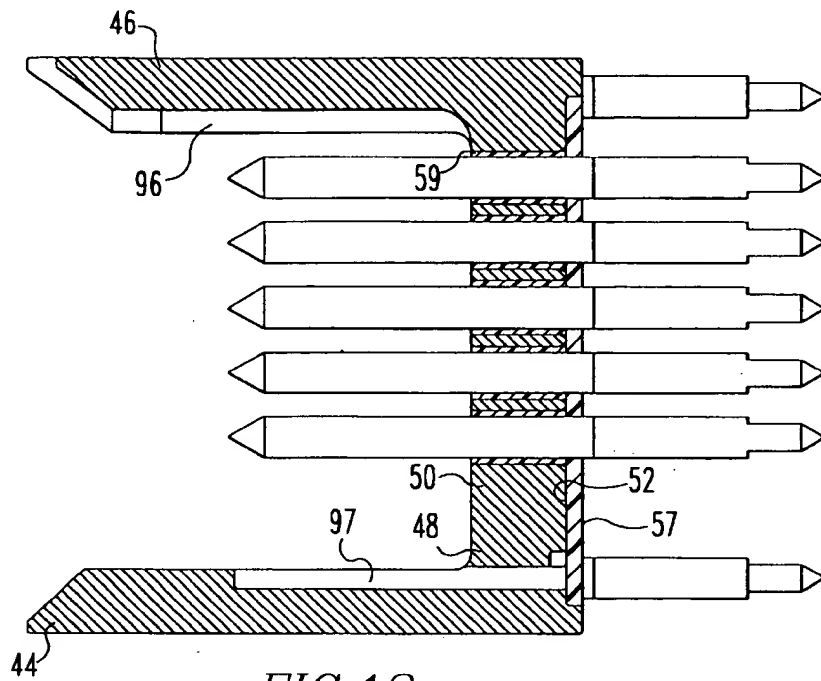


FIG. 12

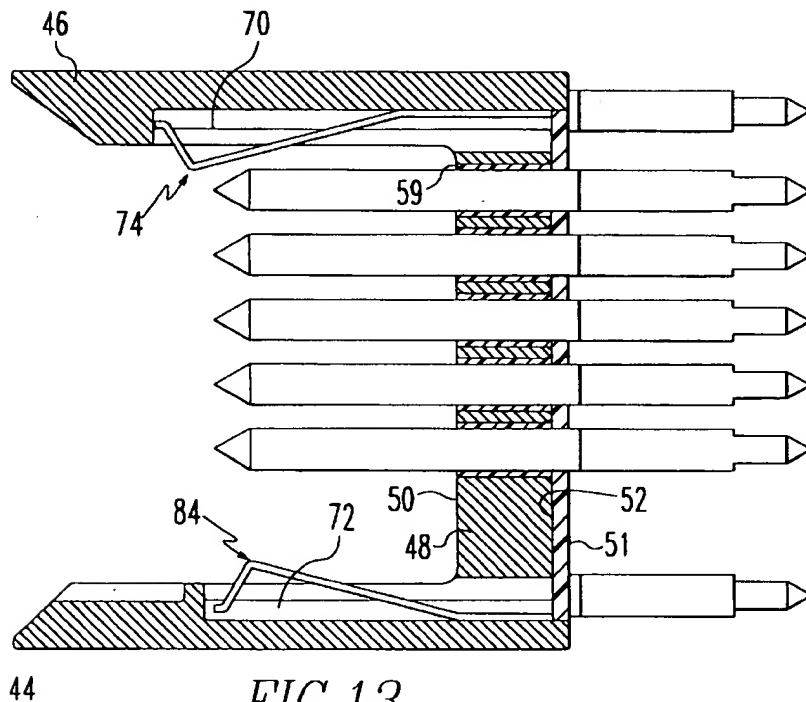
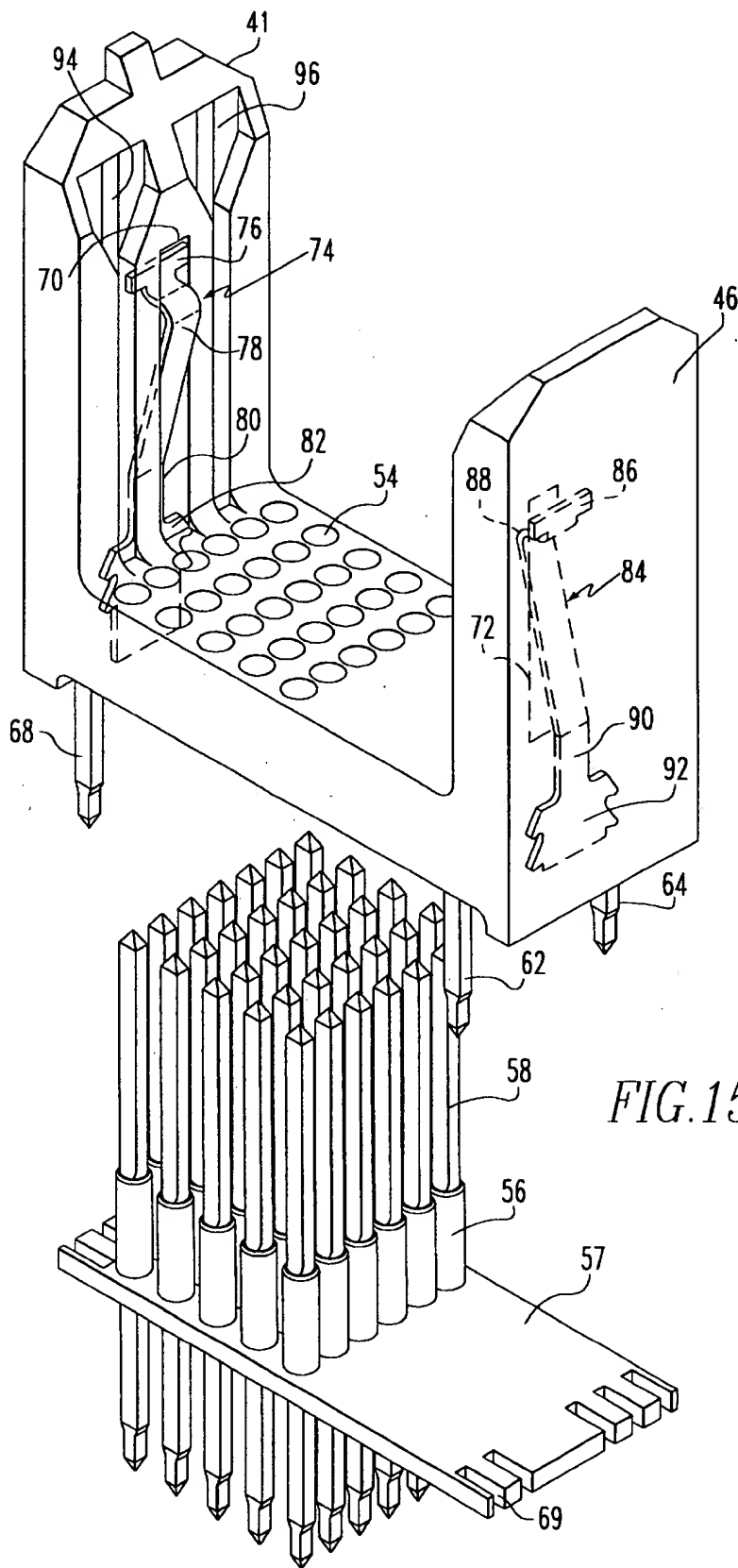


FIG. 13



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## ELECTRICALLY ENHANCED MODULAR CONNECTOR FOR PRINTED WIRING BOARD

This application is a division of application Ser. No. 08/417,085, filed Apr. 4, 1995 now U.S. Pat. No. 5,967,844.

### BACKGROUND OF THE INVENTION

#### 1. Field of the Invention

The present invention relates to electrical connectors and more particularly to modular connectors for use in connecting a daughter printed wiring board to a mother printed wiring board.

#### 2. Brief Description of the Prior Developments

In the manufacture of computers and other various electronic assemblies, daughter boards are commonly connected to mother boards by means of a connector having a receptacle having a plastic housing and a first and second face wherein terminals are connected in one face to the daughter board and at the other to a header connected to the mother board. Various arrangements have been suggested to ground such connectors to the mother or daughter boards but such arrangements have tended to complicate the construction of the connector. A need, therefore, exists for simple and inexpensive means for grounding connectors between mother and daughter boards. There is also a need for such a connector which reduces crosstalk and EMI.

### SUMMARY OF THE INVENTION

In the electrical connector of the present invention a receptacle is connected to a daughter board. This receptacle has a housing having a first face and a second face and a plurality of terminals extending from the first face of the daughter board to the second face where there is an interface with a shielded header. The header has two end walls and a medial wall and is comprised of a conductive material, preferable a suitable metallic alloy. A plurality of apertures extend through the medial wall and retain signal pins which contact the terminals in the receptacle. There is a first and second face on the medial wall. The first face interfaces with the second face of the receptacle. The second face abuts the printed wiring board. On the second face there are a plurality of recesses into which conductive pins are press fitted to ground the connector. On the inner side of the end walls of the header there are also axial grooves which retain removable springs which contact shields on the receptacle to further aid in grounding the connector.

### BRIEF DESCRIPTION OF THE INVENTION

FIG. 1 is a side elevational view of the receptacle element of the connector of the present invention;

FIG. 2 is a side elevational view of the header element of the connector of the present invention;

FIG. 3 is a side elevational view of the engaged receptacle and header elements to form the connector of the present invention;

FIG. 4 is a bottom view of the receptacle element shown in FIG. 1;

FIG. 5 is a front end view of the receptacle shown in FIG. 1;

FIG. 6 is a perspective view of the receptacle shown in FIG. 1;

FIG. 7 is a detailed view of a corresponding area in FIG. 3 with the addition of an affixed printed wiring board in fragment;

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FIG. 8 is a top plan view of the header shown in FIG. 2;

FIG. 9 is a front view of the header shown in FIG. 2;

FIG. 10 is a bottom plan view of the header shown in FIG. 2;

FIG. 11 is a rear view of the header shown in FIG. 2;

FIG. 12 is a cross sectional view taken through line XII—XII in FIG. 8;

FIG. 13 is a cross sectional view taken through line XIII—XIII in FIG. 8;

FIG. 14 is a detailed view of the area in circle XIV of FIG. 11;

FIG. 15 is a perspective view of the header shown in FIG. 2.

### DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENT

Referring to the figures, the receptacle is shown generally at numeral 10, the shielded header is shown generally at numeral 12. Referring particularly to the FIGS. 1 through 6, the receptacle includes a housing generally at numeral 14 with a first planar face 16, a second planar face 18. Terminals as at 20 extend from the first planar face to the second planar face through passageways as at 21 and then through cavities as at 22. The housing includes top metallic shield 24 and a bottom metallic shield 26. It also includes alignment ribs 28 and 30 and code key holders 32 and 34. Grounding is effected through ground pins 36, 37 and 38 and press peg 40 is used to fix receptacle to a daughter printed wiring board 42 along with the terminals. Referring particularly to FIGS. 2 and 8 through 15, the header includes end walls 44 and 46 and medial wall 48 which is perpendicularly interposed between the end walls. The medial wall includes a first planar face 50 and a second planar face 52. The header 10 can be die cast of a suitable metallic alloy. Passageways as at 54 extend transversely across the medial wall from the first planar face to the second planar face and are equipped with insulative sleeves as at 56 which are integrated with an insulative plate 57. Conductive signal pins as at 58 extend through these passageways to engage the terminals as at 20 positioned in cavities as 22 in the receptacle. On the second planar face of the medial wall there are recesses as at 59 and 60 which extend only partially through this wall and which receive grounding pins as at 62 and 64 by press fit. As is particularly shown in FIG. 14, the signal pins have shoulder 63 and base insulation rings 65. Other grounding pins 66 and 68 engage other similar recesses in the second planar face of the medial wall. It will be appreciated that additional recesses may be used so that there will be more than one potential position for each grounding pin and so that the grounding pins can be selectively positioned. The grounding pins pass through slots as at 69 in the insulative plate 57. On the inner side of the end walls 44 and 46 there are respectively central vertical grooves 70 and 72. In central vertical groove 70 there is a metallic contact spring 74 which is comprised of a top locking section 76 which engages the side of the groove and is press fit into the groove. A concave section 78 which extends inwardly then back toward the groove, a vertical section 80 and a lower locking section 82 which also engages the groove and which is press fit into the groove. The vertical groove 72 also has a metallic spring 84 which similarly has a top locking section 86, a concave section 88, a vertical section 90 and a lower lock section 92. On each side of the vertical groove 70 there is a lateral vertical groove 94 and 96 which are alignment features that engage ribs 28 and 30 on the receptacle. The spring contacts 74 and 84 are removable from the grooves in which they are

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mounted and can be optionally used, depending upon the specific characteristics of the receptacle with which they are to be used. The end wall 46 also includes a groove 97 which is useful in the manufacturing of the connector but which has no function thereafter. This end wall also has a recess 98 on its inner side to accommodate the press pin 40. As is particularly shown in FIG. 10, it will be understood that the pins of the header are connected to a mother printed wiring board 100 through apertures as at 102.

The connector as described above is considered to have surprisingly and unexpectedly good crosstalk and EMI reduction characteristics. This connector is also easily and inexpensively grounded to the printed wiring boards to which it is attached.

While the present invention has been described in connection with the preferred embodiments of the various figures, it is to be understood that other similar embodiments may be used or modifications and additions may be made to the described embodiment for performing the same function of the present invention without deviating therefrom. Therefore, the present invention should not be limited to any single embodiment, but rather construed in breadth and scope in accordance with the recitation of the appended claims.

What is claimed is:

1. An electrical connector comprising a receptacle comprising a housing having a first face and a second face and at least one exterior conductive shield is interposed between said first face and said second face and a plurality of conductive elements comprising receptacle contact termi-

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nals which extend from said first face of said receptacle to said second face of said receptacle; and a header having a conductive housing comprising a medial wall and generally parallel end walls with opposed inner faces wherein one of said opposed inner faces of the header abuts the exterior conductive shield of the receptacle and a medial wall is interposed between said opposed generally parallel end walls, and said medial wall has a first face and a second face wherein said first face of the medial wall is adjacent to the second face of the receptacle and a plurality of passages extend between the first and second faces of the medial wall and there is a plurality of conductive signal pins and each of said plurality of conductive signal pins passes through one of said passages between the first and second faces of the medial wall to engage a printed wiring board and an insulative plate having a generally vertical plane fitted in a groove of said second face of said medial wall with a plurality of insulative sleeves which are integral with said insulative plate and which extend generally perpendicularly therefrom and each of said insulative sleeves is positioned in one of the passages between the first and second faces of the medial wall such that one of said insulative sleeves is interposed between each of said conductive signal pins and the medial wall and a grounding pin is attached to the conductive housing of the header and extends from said conductive housing and passes through the generally vertical plane of said insulative plate to engage the printed wiring board.

\* \* \* \* \*